

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	113	((transparent latch) or (master slave (latch or (flip flop)))) and "713".clas.	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 14:51
L4	0	((transparent latch) or (master slave (latch or (flip flop)))) and (VLIW or very long instruction word) and "713".clas.	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 14:42
L5	232	(VLIW or very long instruction word) and "713".clas.	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 14:48
L6	29	(VLIW or very long instruction word) and (713/320.ccls. or 713/324.ccls or 713/330.ccls)	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 14:49
L7	6	((transparent latch) or (master slave (latch or (flip flop)))) and (713/320.ccls. or 713/324.ccls or 713/330.ccls)	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 14:57
L8	13	isolat\$4 (functional or arithmetic or (fixed or floating) point or load store) unit	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 15:07
L9	0	((functional or arithmetic or (fixed or floating) point or load store) unit input with hold) and "713".clas.	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 15:08
L10	8	((functional or arithmetic or (fixed or floating) point or load store) unit input) and "713".clas.	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 15:22
L11	1	((functional or arithmetic or (fixed or floating) point or load store) unit input) with latch point	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 15:42
L12	2	((("6122722") or ("6002880"))).PN.	USPAT; USOCR	OR	OFF	2007/09/22 15:30
L13	11	("4722084" "4890285" "4970724" "5276893" "6021511" "6067633" "6697979" "6730527" "6785841" "7017074" "7185225").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/09/22 15:40
L14	0	((functional or arithmetic or (fixed or floating) point or load store) unit input) with operand register	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 15:42
L15	90	((functional or arithmetic or (fixed or floating) point or load store) unit) with operand register	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 17:05
L16	1	("6697979").PN.	USPAT; USOCR	OR	OFF	2007/09/22 16:01
L17	13	"196 51 075.9-53"	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 16:02

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L18	396	latch point	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 16:02
L19	9	latch point and "713".clas.	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 16:07
L20	1	latch point same ((functional or arithmetic or (fixed or floating) point or load store) unit)	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 16:08
L21	13	latch point and ((functional or arithmetic or (fixed or floating) point or load store) unit)	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 16:08
L22	54	((functional or arithmetic or (fixed or floating) point or load store) unit) with leakage	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 17:05
L23	29	((functional or arithmetic or (fixed or floating) point or load store) unit) with clock gating	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 17:06
L24	0	((functional or arithmetic or (fixed or floating) point or load store) unit input) with (clock gating or power leak\$4)	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 17:06
L25	1	(input with (functional or arithmetic or (fixed or floating) point or load store) unit) with (clock gating or power leak\$4)	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 17:17
L26	6	(input with (functional or arithmetic or (fixed or floating) point or load store) unit) same (clock gating or power leak\$4)	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 17:18
S33	2485	(transparent latch) or (master slave (latch or (flip flop)))	US-PGPUB; USPAT	ADJ	OFF	2007/09/22 14:39
S34	977	(transparent latch) or (master slave (latch or (flip flop))) and ((routing or switching) (network or matrix) with register)	US-PGPUB; USPAT	ADJ	OFF	2007/09/21 14:25
S35	974	(transparent latch) or (master slave (latch or (flip flop))) and ((routing or switching) (network or matrix) with register) and "713".ccls.	US-PGPUB; USPAT	ADJ	OFF	2007/09/21 14:30
S36	974	(transparent latch) or (master slave (latch or (flip flop))) and ((routing or switching) (network or matrix) with register near2 input)	US-PGPUB; USPAT	ADJ	OFF	2007/09/21 14:34
S38	4	((transparent latch) or (master slave (latch or (flip flop))) and ((routing or switching) (network or matrix) with register)) and (instruction set with interrupt)	US-PGPUB; USPAT	ADJ	OFF	2007/09/21 14:31

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S39	0	((transparent latch) or (master slave (latch or (flip flop)))) and ((routing or switching) (network or matrix) with register)) and "713". ccls.	US-PGPUB; USPAT	ADJ	OFF	2007/09/21 14:30
S40	4	((transparent latch) or (master slave (latch or (flip flop)))) and ((routing or switching) (network or matrix) with (function\$3 or arithmetic) unit)) and (instruction set with interrupt)	US-PGPUB; USPAT	ADJ	OFF	2007/09/21 14:32
S41	0	((transparent latch) or (master slave (latch or (flip flop)))) and ((routing or switching) (network or matrix) with register near2 input)	US-PGPUB; USPAT	ADJ	OFF	2007/09/21 14:34
S42	42	((transparent latch) or (master slave (latch or (flip flop)))) and ((routing or switching) (network or matrix))	US-PGPUB; USPAT	ADJ	OFF	2007/09/21 14:37
S43	0	((transparent latch) or (master slave (latch or (flip flop)))) and (register near2 output with (functional or arithmetic) unit near2 input)	US-PGPUB; USPAT	ADJ	OFF	2007/09/21 14:39
S45	2485	((transparent latch) or (master slave (latch or (flip flop))))	US-PGPUB; USPAT	ADJ	OFF	2007/09/21 14:42
S46	27	((transparent latch) or (master slave (latch or (flip flop)))) and ((functional or arithmetic) unit near2 input)	US-PGPUB; USPAT	ADJ	OFF	2007/09/21 14:41